

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Sheet		of		Application Number	09/945,266
				Filing Date	August 31, 2001
				First Named Inventor	Richard Coulson
				Art Unit	2186
				Examiner Name	W. Choi
				Attorney Docket Number	42390P11446

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number Number - Kind Code <sup>§</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
WLC		US-6012140	01-04-2000	THOMASON	
		US-6023713	02-08-2000	GRIMSRUD et al	
		US-6052789	04-18-2000	LIN	
		US-6101574	08-08-2000	KUMASAWA et al	
		US-6122711	09-19-2000	MACKENTHUN et al	
		US-6165006	12-26-2000	YEH et al	
		US-6175160	01-16-2001	PANICCIA et al	
		US-6178479	01-23-2001	VISHIN	
		US-6295577	09-25-2001	ANDERSON et al	
		US-6438647	08-20-2002	NIELSON et al	
		US-2004/0162950	08-19-2004	COULSON	
		US-2004/0225835	11-11-2004	COULSON	
		US-			
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.†	Foreign Patent Document Country Code <sup>§</sup> - Number <sup>§</sup> - Kind Code <sup>§</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*

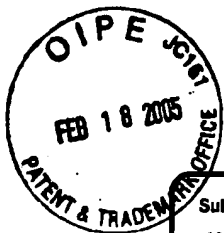
Examiner Signature		Date Considered	4/8/05
-----------------------	--	--------------------	--------

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number (optional). \*See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. \*Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). \*For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. \*Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \*Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Sheet

of

Complete if Known

Application Number	09/945,266
Filing Date	August 31, 2001
First Named Inventor	Richard Coulson
Art Unit	2186
Examiner Name	W. Choi
Attorney Docket Number	42390P11446

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	7
Whe		COULSON - US PATENT APPLICATION 09/602,008 "IN-LINE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,009 "NON-VOLATILE CACHE EXPANSION BOARD"	
		COULSON - US PATENT APPLICATION 09/602,010 "NON-VOLATILE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,011 "NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE"	
		COULSON - US PATENT APPLICATION 10/740,633 "NON-VOLATILE MASS STORAGE CACHE COHERENCY APPARATUS ADMINISTRATOR"	
		AUCIELLO et al - THE PHYSICS OF FERROELECTRIC MEMORIES, Physics Today, July 1998, pp 22-27	
		SGS THOMSON - GSF32-16x16/90 32 MByte SIMM Glash Memory Module 4 pages	
		DERWENT - DOCUMENT ID RD 431135 A - "A raid controller with a removable NVRAM cache on detecting a battery or an NVRAM fault automatically flushes all...3 pages	
		DEROWITSCH - Smart Computing - "The Quiet Role of Chipsets" Aug. 1998, Vol 9, Issue 8	
		DIEFENDORFF - Intel Tries Integrating Graphics - In-Star MDR Aug. 24, 1998	
		DPT - Smart IV PM2144W PCI High Performance SCSI Adapter	
		DPT - News Release - DPT's SmartCache IV RAID/Caching Kits Outperform Adaptec's SCSI Adapters in Independent Tests and Reviews - Aug. 1, 1996	
		HODGES et al - 1983, pp 388-389 - Analysis and Design of Digital Integrated Circuits	
		INTEL - June 1997 - Intel 430HX PCIsset Design Guide pp 1-1 thru 6-15	

Examiner Signature		Date Considered	4/8/05
-----------------------	--	--------------------	--------

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. \*Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Sheet

of

*Complete if Known*

Application Number	09/945,266
Filing Date	August 31, 2001
First Named Inventor	Richard Coulson
Art Unit	2186
Examiner Name	W. Choi
Attorney Docket Number	42390P11446

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
WLC		INTEL - April 1997 - 82371FB (PIIX) AND 82371SB (PIIX3) PCI ISA IDE XCELERATOR	
		IRE TRANSACTIONS - COMPONENT PARTS - Switching the Memory Matrix	
		MERZ - Sept. 1955 - Ferroelectric Storage Devices pp 335-342	
		MICROSOFT PRESS - Computer Dictionary 2nd Ed. 1994 - Nondestructive readout, page 270	
		MOAZZAMI et al - Oct 1990, Vol. 11, Issue 10 Citation and Abstract IEEE/IEE Electronic Library - A ferroelectric DRAM cell for high-density NVRAMs	
		MUELLER - 2002 - UPGRADING AND REPAIRING PCS - pages 363-366 Chapter 9 - Chipset Evolution	
		NTIS - Dept of Navy - Jan 14, 1974 - MULTI-WIRE CABLE TO COAXIAL CABLE TRANSITION APPARATUS - ADD000450, 8 pages	
		IBM Technical Disclosure Bulletin, Jul. 1982 - OPTICAL/MAGNETIC STORAGE DISK SYSTEM, Vol 25, Issue 2 pages 459-460	
		PULVARI - Mar. 1956, pp 3-11 - FERROELECTRICS AND THEIR MEMORY APPLICATIONS	
		RAMTRON Dec. 7, 1999 - FM24C64 64Kb FRAM Serial Memory - 15 pages	
		RAMTRON Dec. 7, 1999 - FM1608 64Kb Britewide FRAM Memory - 12 pages	
		RAMTRON Jan 1994 - FRAM Technology - 2 pages	
		ROBERTSON - Sept. 3, 1998 - Hyundai enters Japanese-dominated FeRAM market - 2 pages	
		ROSENBERG - 1987 - Dictionary of Computers, Information Processing & Telecommunications, 2nd Ed. - P. 221	

Examiner Signature		Date Considered	4/8/05
-----------------------	--	--------------------	--------

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.  
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450